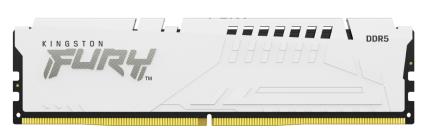
# **Memory Module Specifications**



# KF560C30BWEK2-32

32GB (16GB 2G x 64-Bit x 2 pcs.)
DDR5-6000 CL30 288-Pin DIMM Kit



#### **DEFAULT SPECIFICATIONS**

40 cycles	
48ns(min.)	
295ns(min.)	
32ns(min.)	
32ns(min.) 94 V - 0	
,	

## **DESCRIPTION**

Kingston FURY KF560C30BWEK2-32 is a kit of two 2G x 64-bit (16GB) DDR5-6000 CL30 SDRAM (Synchronous DRAM) 1Rx8, memory module, based on eight 2G x 8-bit FBGA components per module. Each module kit supports AMD® EXPO v1.1 and Intel® Extreme Memory Profiles (Intel® XMP) 3.0. Total kit capacity is 32GB. Each module has been tested to run at DDR5-6000 at a low latency timing of 30-36-36 at 1.4V. The SPDs are programmed to JEDEC standard latency DDR5-4800 timing of 40-39-39 at 1.1V. Each 288-pin DIMM uses gold contact fingers. The JEDEC standard electrical and mechanical specifications are as follows:

#### **DEFAULT FEATURES**

- Power Supply: VDD = 1.1V Typical
- VDDQ = 1.1V Typical
- VPP = 1.8V Typical
- VDDSPD = 1.8V to 2.0V
- On-Die ECC
- Height 1.37" (34.9mm), w/heatsink

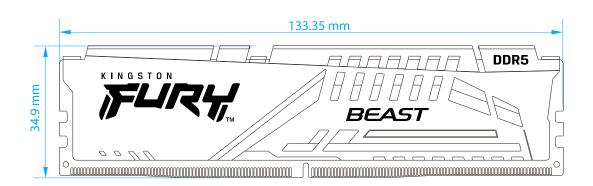
# **FACTORY TIMING PARAMETERS**

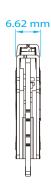
Default (JEDEC): DDR5-4800 CL40-39-39 @1.1V
 EXPO Profile #0: DDR5-6000 CL30-36-36 @1.4V
 EXPO Profile #1: DDR5-5600 CL40-40-40 @1.25V
 XMP Profile #1: DDR5-5600 CL30-36-36 @1.4V
 XMP Profile #2: DDR5-5600 CL40-40-40 @1.25V

Continued >>

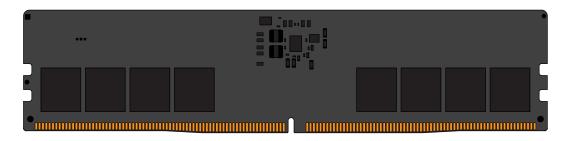


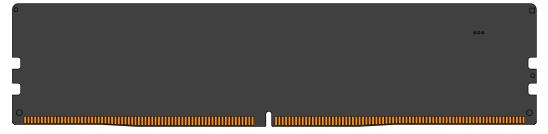
## **MODULE WITH HEAT SPREADER**

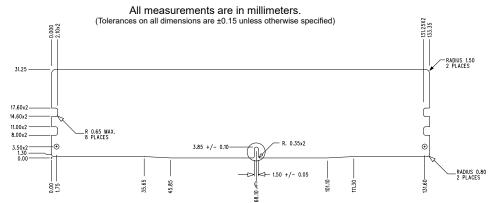




# **MODULE DIMENSIONS**







The product images shown are for illustration purposes only and may not be an exact representation of the product. Kingston reserves the right to change any information at anytime without notice.

## FOR MORE INFORMATION, GO TO KINGSTON.COM

All Kingston products are tested to meet our published specifications. Some motherboards or system configurations may not operate at the published Kingston FURY memory speeds and timing settings. Kingston does not recommend that any user attempt to run their computers faster than the published speed. Overclocking or modifying your system timing may result in damage to computer components.



Revision No.	History	Release Date	Remark	Editor	Approved
Α	Initial Release	02/15/24		David Y.	Alex S.

<sup>\*</sup>Products and specifications discussed herein are for evaluation and reference purposes only and are subject to change without notice. All information discussed herein is provided on an "as is" basis, without warranties of any kind.

Rev. A